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APPLICANTS									
Peter D. Brev	wer, W	estlake Village, CA;							
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TITLE				*		J=1/=			1.50
1	nethod	for integrating device and	l circuit co	mponents		r			
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FILING FEE	FEES:	ES: Authority has been given in Paper				☐ 1.16 Fees (Filing) ☐ 1.17 Fees (Processing Ext. of time)			
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